Refine Search

Search Results -

Terms	Documents
L18 and (devic$ nerar5 test$)	0

Database:
US Pre-Grant Publication Full-Text Database
US Patents Full-Text Database
US OCR Full-Text Database
EPO Abstracts Database
JPO Abstracts Database
Derwent World Patents Index
IBM Technical Disclosure Bulletins

Search: L19

Refine Search

Recall Text Clear Interrupt

Search History

DATE: Wednesday, July 21, 2004 Printable Copy Create Case

Set Name side by side	Query	Hit Count	Set Name result set
	DB=USPT; PLUR=YES; OP=ADJ		
L19	L18 and (devic$ nerar5 test$)	0	L19
L18	(generat$ or creat$) near6 (built-in-self-test) and test$ near5 (compress$ or compac$)	3	L18
	DB=TDBD; PLUR=YES; OP=ADJ		
L17	(test$ near5 execut$) and (repeat$ or regener$)and (random near5 instruction$) and test$ near6 ((on board)or (built-in))and (under$ near5 test$)	0	L17
	DB=DWPI; PLUR=YES; OP=ADJ		
L16	(test$ near5 execut$) and (repeat$ or regener$)and (random near5 instruction$) and test$ near6 ((on board)or (built-in))and (under$ near5 test$)	0	L16
	DB=JPAB; PLUR=YES; OP=ADJ		
L15	(test$ near5 execut$) and (repeat$ or regener$)and (random near5 instruction$) and test$ near6 ((on board)or (built-in))and (under$ near5 test$)	0	L15
	DB=EPAB; PLUR=YES; OP=ADJ		

L14	(test$ near5 execut$) and (repeat$ or regener$)and (random near5 instruction$) and test$ near6 ((on board)or (built-in))and (under$ near5 test$)	0	L14
DB=USOC; PLUR=YES; OP=ADJ			
L13	(test$ near5 execut$) and (repeat$ or regener$)and (random near5 instruction$) and test$ near6 ((on board)or (built-in))and (under$ near5 test$)	0	L13
DB=PGPB; PLUR=YES; OP=ADJ			
L12	(test$ near5 execut$) and (repeat$ or regener$)and (random near5 instruction$) and test$ near6 ((on board)or (built-in))and (under$ near5 test$)	3	L12
DB=USPT; PLUR=YES; OP=ADJ			
L11	L10 and (under$ near5 test$)	8	L11
L10	L9 and test$ near6 ((on board)or (built-in))	10	L10
L9	(test$ near5 execut$) and (repeat$ or regener$)and (random near5 instruction$)	178	L9
L8	L7 AND (DEVIC$ NEAR4 UNDER NEAR4 TEST)	0	L8
L7	BUILT-IN-TEST AND GENERAT$ AND REGENERAT$	14	L7
L6	716/1.CCLS.	671	L6
L5	714/30,33,734.CCLS.	1016	L5
L4	717/124,126.CCLS.	332	L4
L3	717124,126.CCLS.	0	L3
L2	L1 and (regenera) near6 test$	0	L2
L1	((built-in-self-test) or sbe) and (merg$ or combin$) near8 expected$	9	L1

END OF SEARCH HISTORY

Refine Search

Search Results -

Terms	Documents
(714/733 |714/741).ccls.	736

Database:
US Pre-Grant Publication Full-Text Database
US Patents Full-Text Database
US OCR Full-Text Database
EPO Abstracts Database
JPO Abstracts Database
Derwent World Patents Index
IBM Technical Disclosure Bulletins

Search: L20

Refine Search | Recall Text | Clear | Interrupt

Search History

DATE: Wednesday, July 21, 2004 Printable Copy Create Case

Set Name side by side	Query	Hit Count	Set Name result set
DB=USPT; PLUR=YES; OP=ADJ			
L20	714/733,741.ccls.	736	L20
L19	L18 and (devic$ nerar5 test$)	0	L19
L18	(generat$ or creat$) near6 (built-in-self-test) and test$ near5 (compress$ or compac$)	3	L18
DB=TDBD; PLUR=YES; OP=ADJ			
L17	(test$ near5 execut$) and (repeat$ or regener$)and (random near5 instruction$) and test$ near6 ((on board)or (built-in))and (under$ near5 test$)	0	L17
DB=DWPI; PLUR=YES; OP=ADJ			
L16	(test$ near5 execut$) and (repeat$ or regener$)and (random near5 instruction$) and test$ near6 ((on board)or (built-in))and (under$ near5 test$)	0	L16
DB=JPAB; PLUR=YES; OP=ADJ			
L15	(test$ near5 execut$) and (repeat$ or regener$)and (random near5 instruction$) and test$ near6 ((on board)or (built-in))and (under$ near5 test$)	0	L15

DB=EPAB; PLUR=YES; OP=ADJ			
L14	(test$ near5 execut$) and (repeat$ or regener$)and (random near5 instruction$) and test$ near6 ((on board)or (built-in))and (under$ near5 test$)	0	L14
DB=USOC; PLUR=YES; OP=ADJ			
L13	(test$ near5 execut$) and (repeat$ or regener$)and (random near5 instruction$) and test$ near6 ((on board)or (built-in))and (under$ near5 test$)	0	L13
DB=PGPB; PLUR=YES; OP=ADJ			
L12	(test$ near5 execut$) and (repeat$ or regener$)and (random near5 instruction$) and test$ near6 ((on board)or (built-in))and (under$ near5 test$)	3	L12
DB=USPT; PLUR=YES; OP=ADJ			
L11	L10 and (under$ near5 test$)	8	L11
L10	L9 and test$ near6 ((on board)or (built-in))	10	L10
L9	(test$ near5 execut$) and (repeat$ or regener$)and (random near5 instruction$)	178	L9
L8	L7 AND (DEVIC$ NEAR4 UNDER NEAR4 TEST)	0	L8
L7	BUILT-IN-TEST AND GENERAT$ AND REGENERAT$	14	L7
L6	716/1.CCLS.	671	L6
L5	714/30,33,734.CCLS.	1016	L5
L4	717/124,126.CCLS.	332	L4
L3	717124,126.CCLS.	0	L3
L2	L1 and (regenera) near6 test$	0	L2
L1	((built-in-self-test) or sbe) and (merg$ or combin$) near8 expected$	9	L1

END OF SEARCH HISTORY

Terms used **Testing point** and **douglas** Found **10,475** of **139,988**

Sort results by relevance

Display results expanded form

Save results to a Binder

Search Tips

☐ Open results in a new window

Try an Advanced Search

Try this search in The ACM Guide

Results 1 - 20 of 200 Result page: **1** 2 3 4 5 6 7 8 9 10 next

Best 200 shown Relevance scale

1 Testing pointing device performance and user assessment with the ISO 9241, Part 9 standard

Sarah A. Douglas, Arthur E. Kirkpatrick, I. Scott MacKenzie

May 1999 **Proceedings of the SIGCHI conference on Human factors in computing systems: the CHI is the limit**

Full text available: pdf(918.43 KB) Additional Information: full citation, references, citings, index terms

Keywords: Fitts' law, ISO 9241 standard, ergonomic evaluation, isometric joystick, pointing devices, touchpad

2 Accuracy measures for evaluating computer pointing devices

I. Scott MacKenzie, Tatu Kauppinen, Miika Silfverberg

March 2001 **Proceedings of the SIGCHI conference on Human factors in computing systems**

Full text available: pdf(311.42 KB) Additional Information: full citation, abstract, references, citings, index terms

In view of the difficulties in evaluating computer pointing devices across different tasks within dynamic and complex systems, new performance measures are needed. This paper proposes seven new accuracy measures to elicit (sometimes subtle) differences among devices in precision pointing tasks. The measures are target re-entry, task axis crossing, movement direction change, orthogonal direction change, movement variability, movement error, and movement offset. Unlike movement time, error ra ...

Keywords: computer pointing devices, cursor positioning tasks, performance evaluation, performance measurement

3 Gazing and frowning as a new human--computer interaction technique

Veikko Surakka, Marko Illi, Poika Isokoski

July 2004 **ACM Transactions on Applied Perception (TAP)**, Volume 1 Issue 1

Full text available: pdf(213.95 KB) Additional Information: full citation, abstract, references, index terms

The present aim was to study a new technique for human--computer interaction. It combined the use of two modalities, voluntary gaze direction and voluntary facial muscle

activation for object pointing and selection. Fourteen subjects performed a series of pointing tasks with the new technique and with a mouse. At short distances the mouse was significantly faster than the new technique. However, there were no statistically significant differences at medium and long distances between the techniqu ...

Keywords: Gaze direction, electromyography, facial muscle activity

4 Input: Smooth Moves: Quantitative analysis of scrolling techniques

Ken Hinckley, Edward Cutrell, Steve Bathiche, Tim Muss

April 2002 **Proceedings of the SIGCHI conference on Human factors in computing systems: Changing our world, changing ourselves**

Full text available: pdf(676.16 KB) Additional Information: full citation, abstract, references, citings, index terms

We propose a formal experimental paradigm designed to help evaluate scrolling interaction techniques. Such a method is needed by interaction designers to quantify scrolling performance, thereby providing a tool to evaluate and improve upon new techniques. We systematically vary the scrolling *distance* as well as the required *tolerance* of scrolling. Distance and tolerance are the parameters of Fitts' Law, which traditionally has been applied to the evaluation of pointing devices in t ...

Keywords: C:D gain, Fitts' Law, document navigation, scrolling

5 Hand eye coordination patterns in target selection

Barton A. Smith, Janet Ho, Wendy Ark, Shumin Zhai

November 2000 **Proceedings of the symposium on Eye tracking research & applications**

Full text available: pdf(559.34 KB) Additional Information: full citation, abstract, references, citings, index terms

In this paper, we describe the use of eye gaze tracking and trajectory analysis in the testing of the performance of input devices for cursor control in Graphical User Interfaces (GUIs). By closely studying the behavior of test subjects performing pointing tasks, we can gain a more detailed understanding of the device design factors that may influence the overall performance with these devices. Our Results show them are many patterns of hand eye coordination at the computer interface which di ...

Keywords: eye tracking, hand eye coordination, motor control, mouse, pointing, pointing stick, target selection, touchpad

6 Paper session #1: Experimental evaluation of vision and speech based multimodal interfaces

Emilio Schapira, Rajeev Sharma

November 2001 **Proceedings of the 2001 workshop on Percetive user interfaces**

Full text available: pdf(581.28 KB) Additional Information: full citation, abstract, references

Progress in computer vision and speech recognition technologies has recently enabled multimodal interfaces that use speech and gestures. These technologies o er promising alternatives to existing interfaces because they emulate the natural way in which humans communicate. However, no systematic work has been reported that formally evaluates the new speech/gesture interfaces. This paper is concerned with formal experimental evaluation of new human-computer interactions enabled by speech and hand ...

7 Motor adaptations: Cursor measures for motion-impaired computer users

Your search matched **0** of **1053485** documents.
A maximum of **500** results are displayed, **15** to a page, sorted by **Relevance Descending** order.

Refine This Search:
You may refine your search by editing the current search expression or enter new one in the text box.

built in self test and regenerationa and test and devi Search

☐ Check to search within this result set

Results Key:
JNL = Journal or Magazine **CNF** = Conference **STD** = Standard

---

Results:
No documents matched your query.

Terms used
built in **self** **test** and **regeneration** and **execute** and **compact** and **device**

Found **20,703** of **139,988**

Sort results by relevance

Display results expanded form

Save results to a Binder

Search Tips

☐ Open results in a new window

Try an Advanced Search

Try this search in The ACM Guide

Results 1 - 20 of 200 Result page: **1** 2 3 4 5 6 7 8 9 10 next

Best 200 shown

Relevance scale

1 Energy-aware design of embedded memories: A survey of technologies, architectures, and optimization techniques

Luca Benini, Alberto Macii, Massimo Poncino

February 2003 **ACM Transactions on Embedded Computing Systems (TECS)**, Volume 2 Issue 1

Full text available: pdf(288.44 KB) Additional Information: full citation, abstract, references, index terms

Embedded systems are often designed under stringent energy consumption budgets, to limit heat generation and battery size. Since memory systems consume a significant amount of energy to store and to forward data, it is then imperative to balance power consumption and performance in memory system design. Contemporary system design focuses on the trade-off between performance and energy consumption in processing and storage units, as well as in their interconnections. Although memory design is as ...

Keywords: Embedded systems, embedded memories, integration, memories, nonvolatile, system-on-a-chip, volatile

2 An automated BIST approach for general sequential logic synthesis

C. E. Stroud

June 1988 **Proceedings of the 25th ACM/IEEE conference on Design automation**

Full text available: pdf(750.11 KB) Additional Information: full citation, abstract, references, citings, index terms

An automated Built-In Self-Test (BIST) technique for general sequential logic is described. This BIST approach has been incorporated in a behavioral model synthesis system providing automated implementation of BIST in Very Large Scale Integration (VLSI) devices as well as Programmable Logic used at all levels of testing from device testing through system diagnostics. The BIST approach is based on selective replacement of existing system memory elements with BIST flip-flop cells that are con ...

3 Trace-driven memory simulation: a survey

Richard A. Uhlig, Trevor N. Mudge

June 1997 **ACM Computing Surveys (CSUR)**, Volume 29 Issue 2

Full text available: pdf(636.11 KB) Additional Information: full citation, abstract, references, citings, index terms, review

As the gap between processor and memory speeds continues to widen, methods for